



# A TOOL-CHAIN FOR FUNCTIONAL SAFETY AND RELIABILITY IMPROVEMENT IN AUTMOTIVE SYSTEMS

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<https://equitas-project.com/site/>

Bpifrance AAP FUI16 and the General Council of Essonne France

## AGENDA

- **Context and Trends**
- **Objectives and Results**
- **Methodology and tools**
  - Technical challenges
  - Automatic test generation
  - Analysis and verification of compliance with the requirements
  - Virtual platforms and HW faults injection
- **Current status – Tools chain V2**
- **Next works**

## CONTEXT AND TRENDS

### ■ $\mu$ Electronic Miniaturization

- Increased sensitivity of ECU to transient hardware faults

### ■ Increased number of functions

- 10-100 ECU (distributed system)

### ■ Hostile operating environment

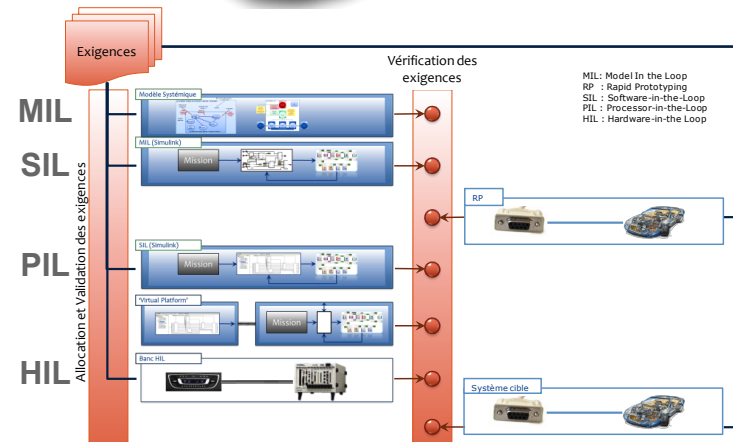
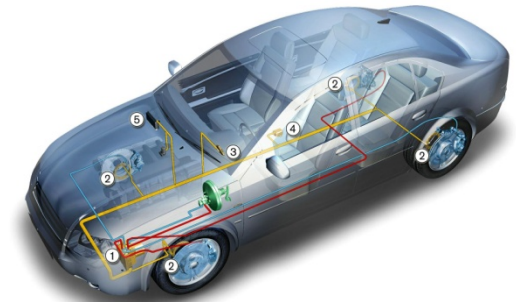
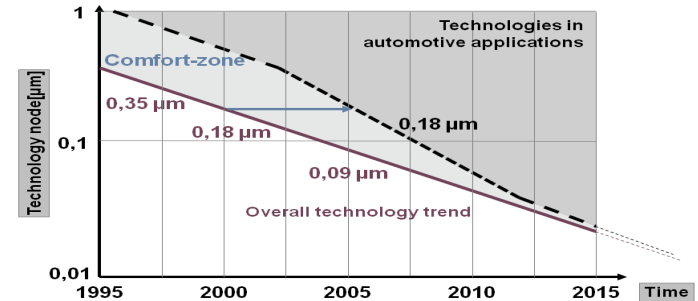
- Electromagnetic fields, temperature, humidity

### ■ ISO26262 standard

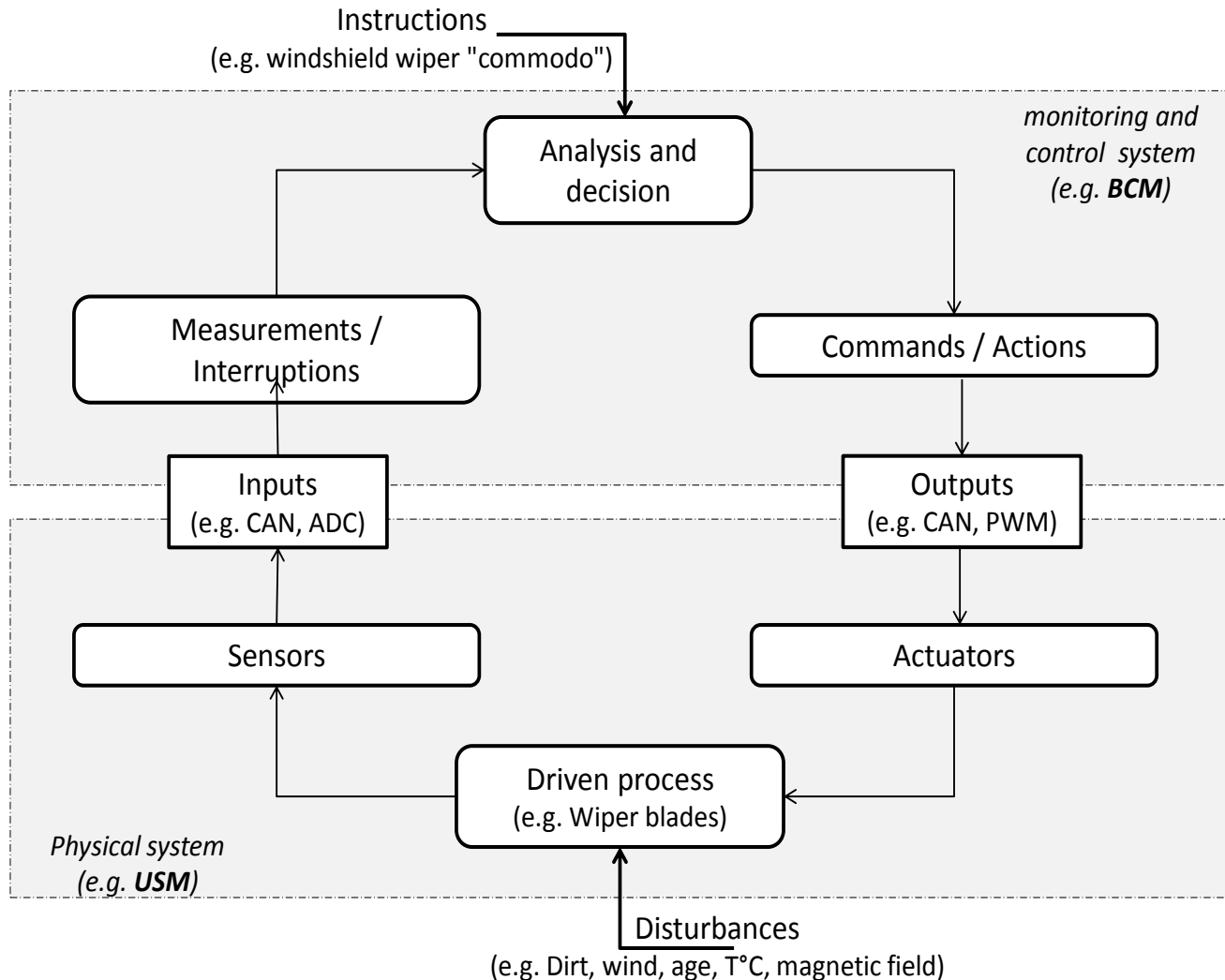
- Safety constraints (product and process)

### ■ Embedded software V&V

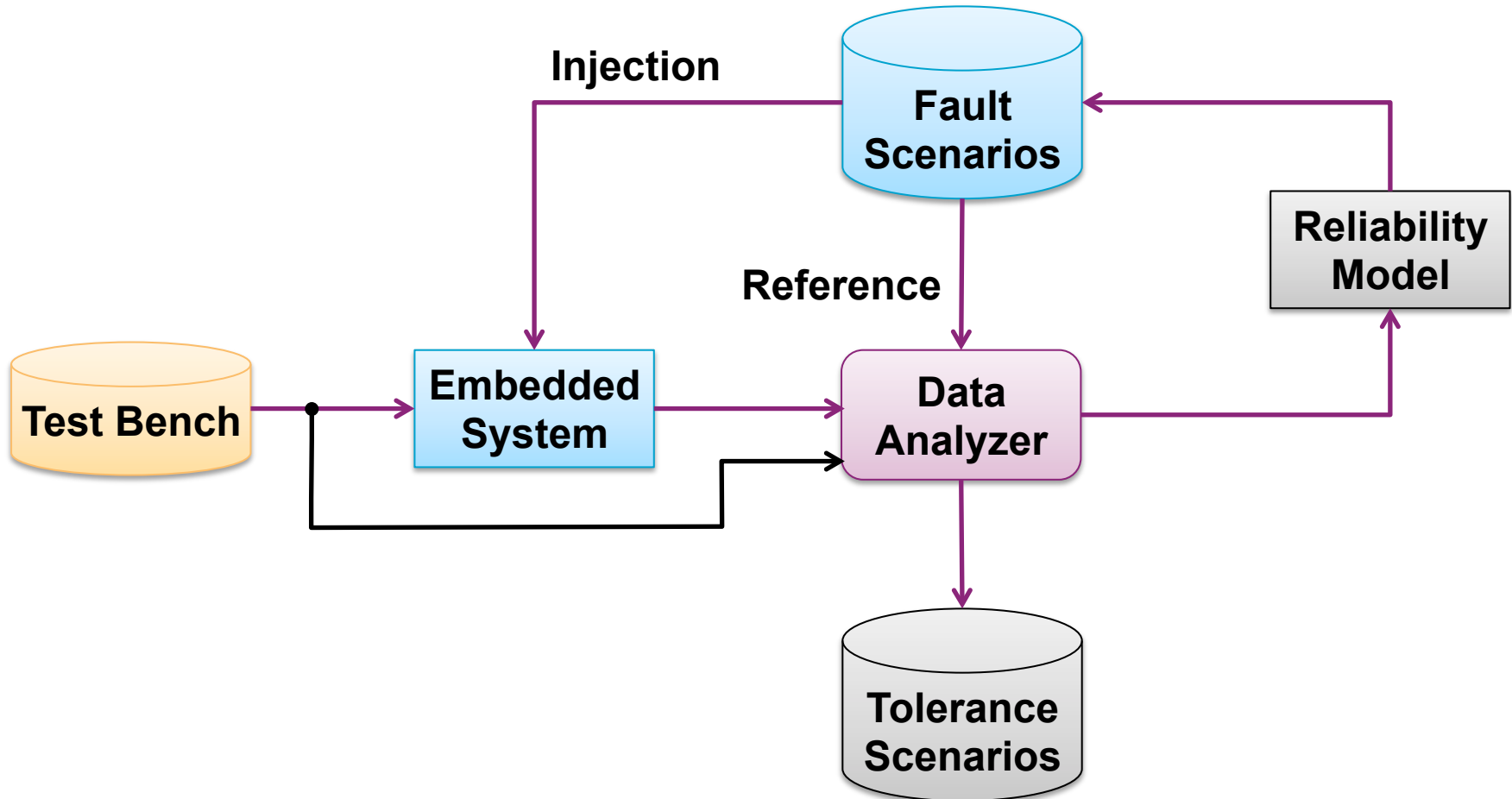
- Complex iterative process
- 40-50% total development cost



## BLOC DIAGRAM OF CONTROL LOOP



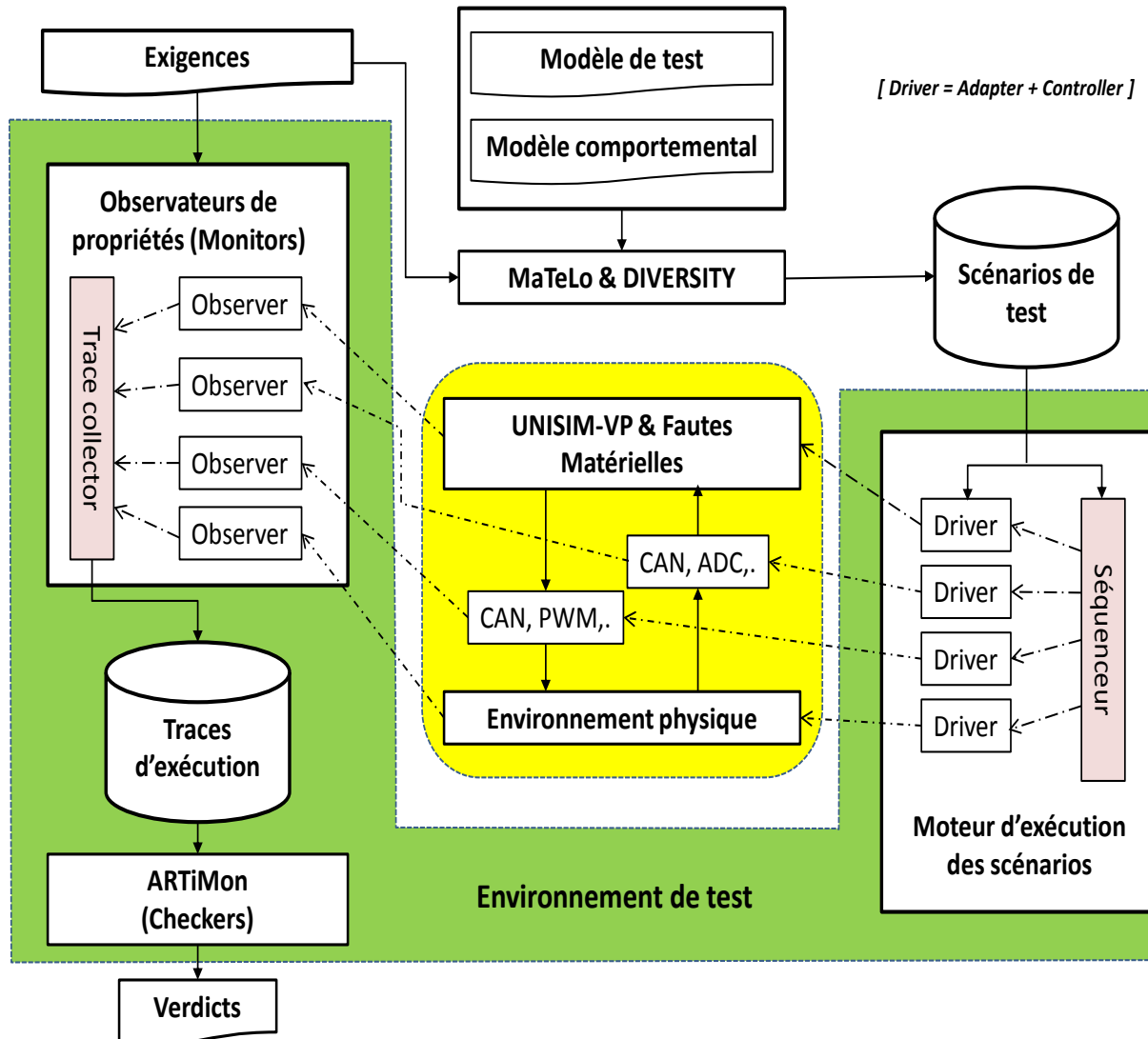
## FUNCTIONAL SAFETY AND RELIABILITY ANALYSIS



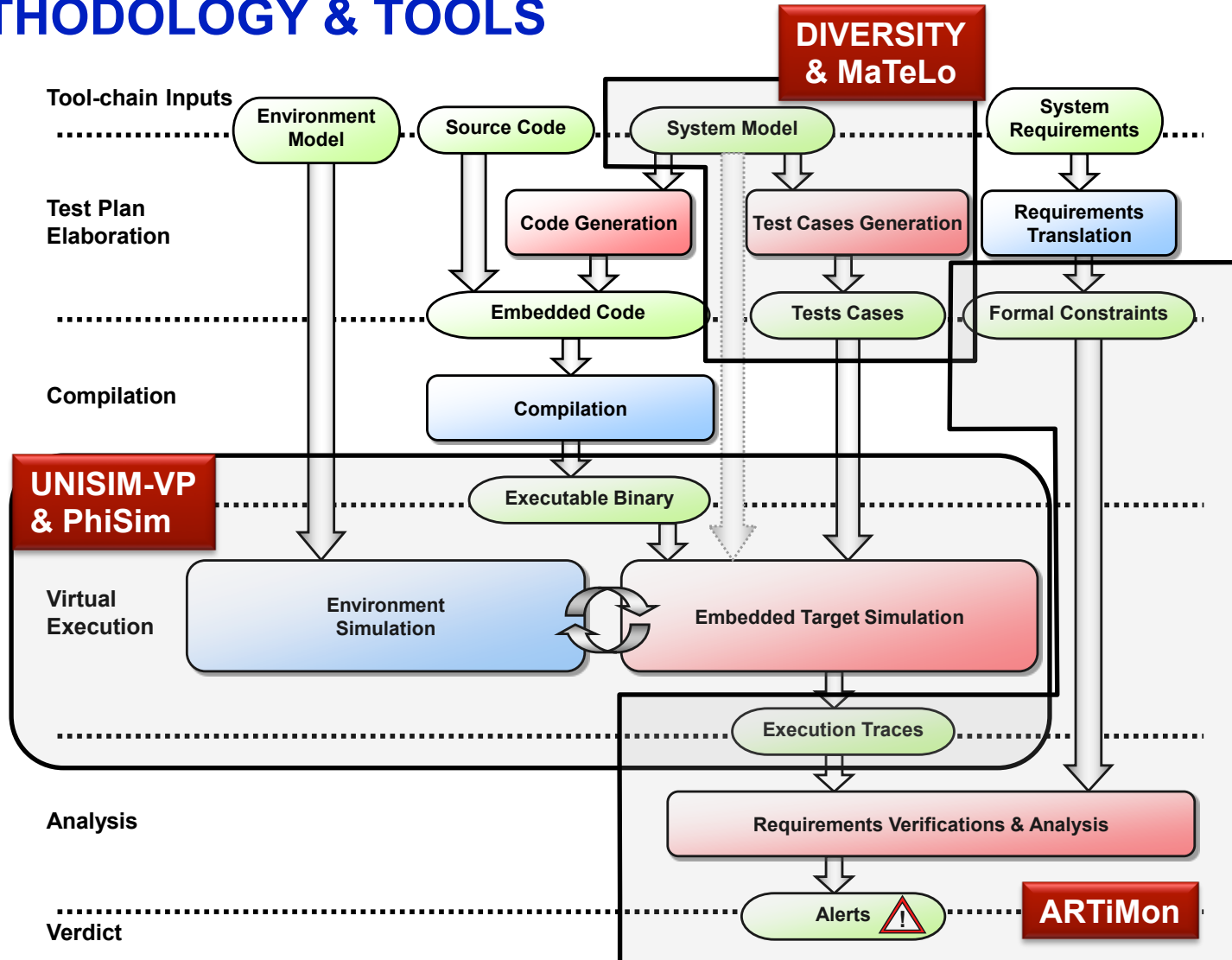
## OBJECTIVES AND RESULTS

- **Automate the verification and validation process of whole embedded software stacks**
  - By developing a continuous tool-chain
  - In the context of automotive electronic systems
- **Improve the relevance of the test campaigns**
  - By detecting the redundant tests using equivalence classes
- **Provide assistance to the hardware failure effect analysis (FMEA)**
  - By introducing a hardware faults model during simulation.
- **Extract a comprehensive V&V methodology using virtual platforms**
  - By assessing the EQUITAS tool chain on real automotive use cases
- **Assess the tool-chain under the ISO 26262 requirements.**

## VIRTUAL TESTING ENVIRONMENT



## METHODOLOGY & TOOLS





## TECHNICAL CHALLENGES

### ■ The use of symbolic execution principle to analyze and reduce test cases obtained by a stochastic approach

#### ■ MaTeLo: stochastic test generation technique

- ⇒ Generate the most likely tests
- ⇒ Many redundant/duplicate tests
- ⇒ Developed by ALL4TEC (<http://www.all4tec.net/MaTeLo/homematelo.html>)

#### ■ DIVERSITY: symbolic execution

- ⇒ Model validation by analyzing its symbolic execution tree
- ⇒ Property verification
- ⇒ Automatic test generation based mainly on the paths coverage
- ⇒ Developed by CEA LIST (<http://projects.eclipse.org/proposals/diversity>)

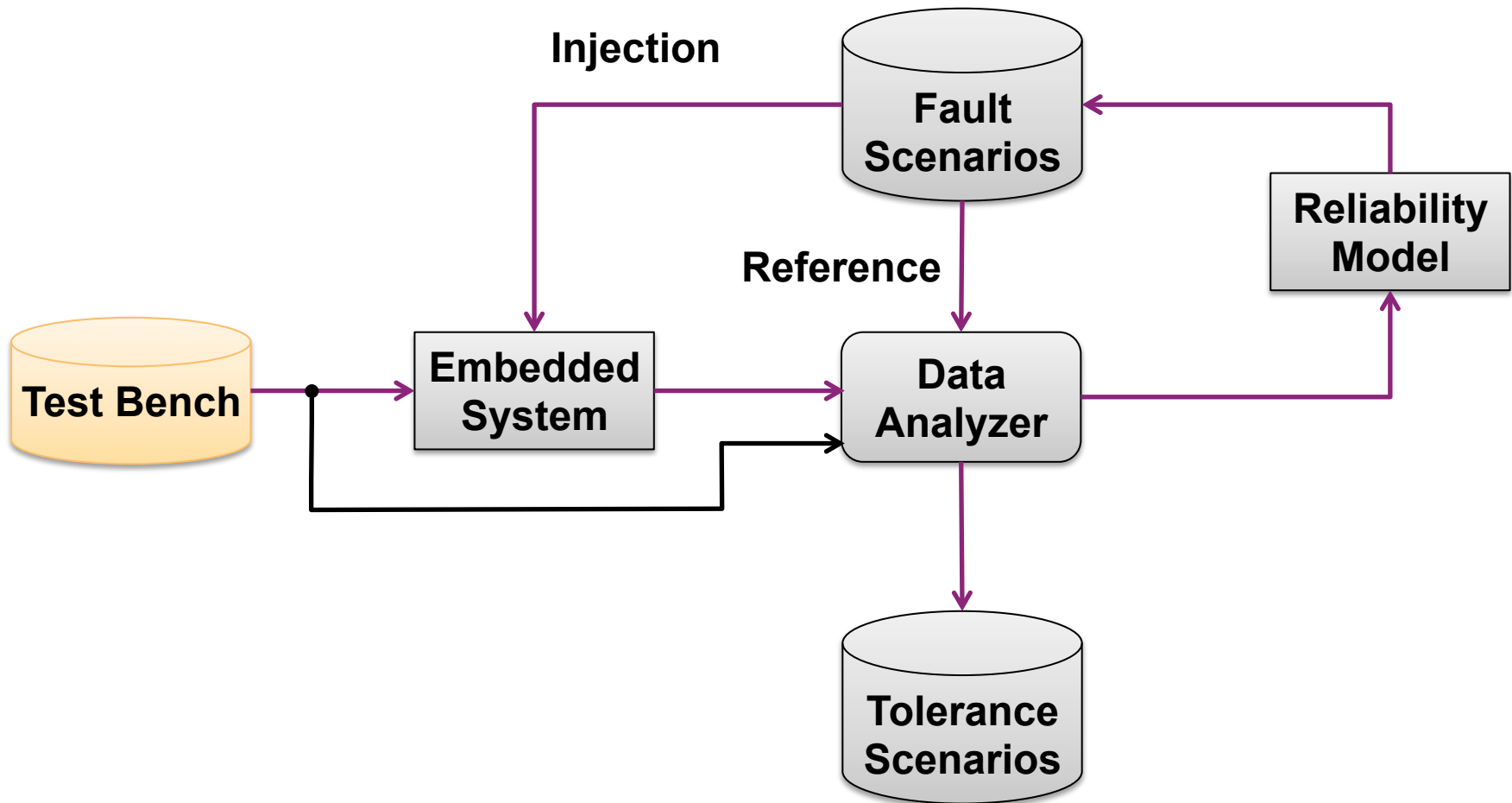
### ■ The extension of the simulation environment UNISIM-VP

#### ■ Modeling and injection of characterised hardware faults

#### ■ Interface to test cases generation tools (MaTeLo & DIVERSITY)

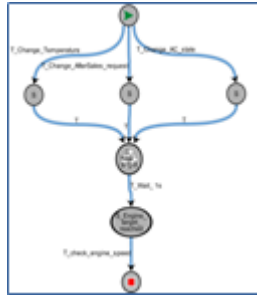
#### ■ Interface to compliance (monitoring) analysis tool (ARTiMon)

## FUNCTIONAL SAFETY AND RELIABILITY ANALYSIS



## MATELO OVERVIEW

### 1 Graphical Design



### 2 Test Case Generation



### 3 Test Script Generation

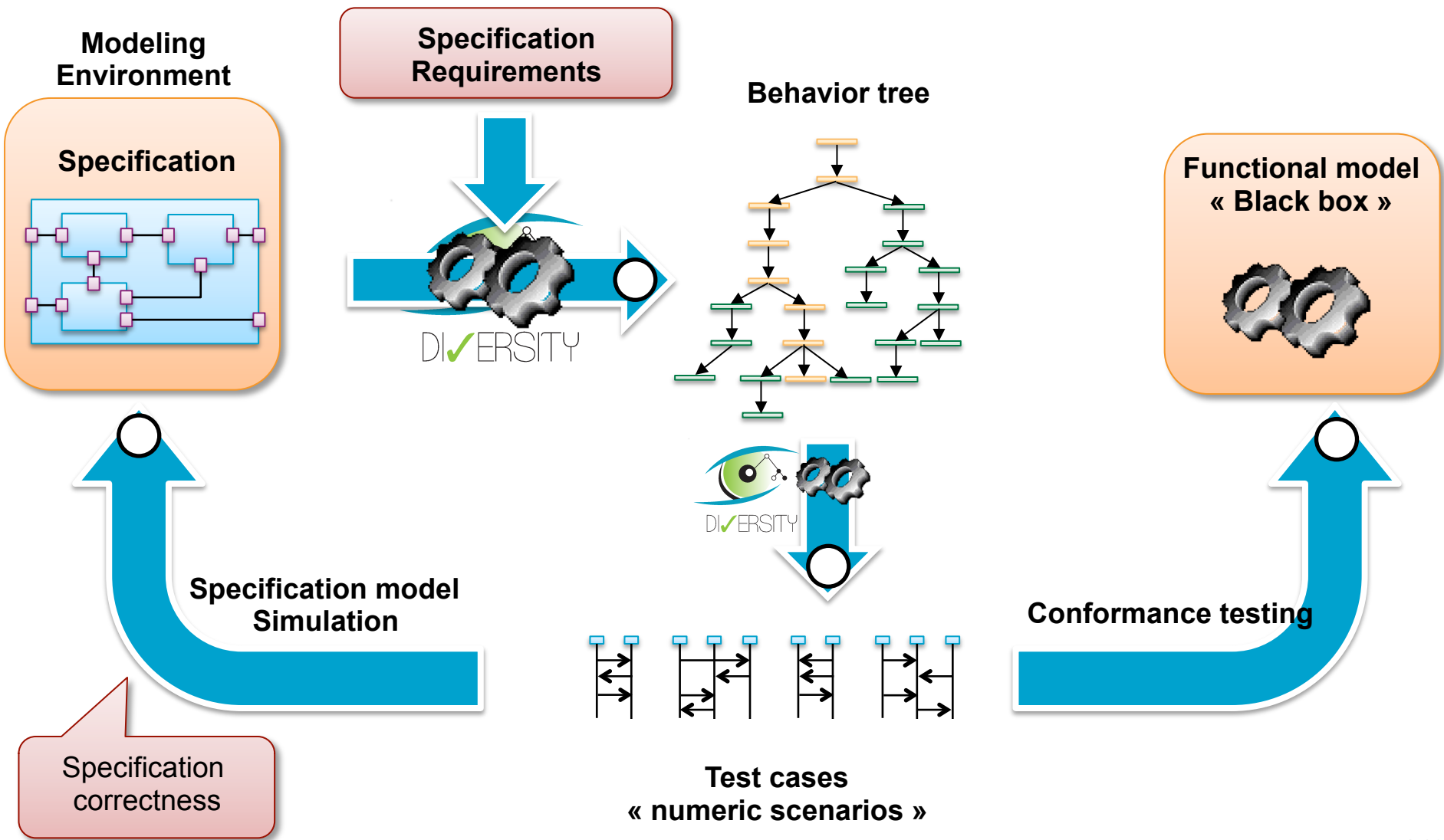


### 4 Coverage Report

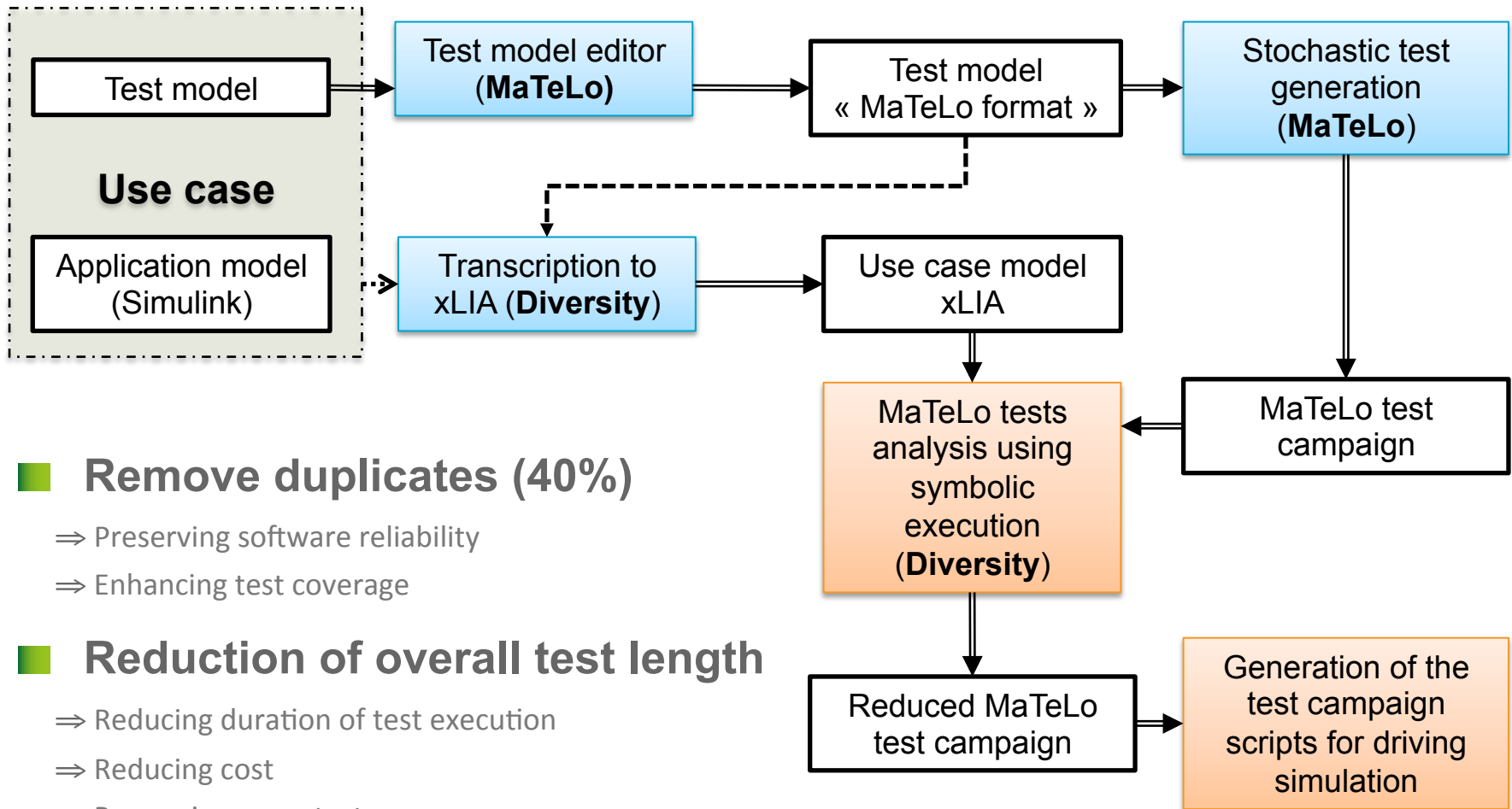
Test Cases Coverage		
Project properties		
Project:	LinuxTypes	No Test Cases: 5
Report date:	20-Nov-2015 09:15	No Errors: 0
Set of Test Cases analysed		
Test Case	Generation Date	Description
Test Case 1	9-Nov-2015 09:20	
Test Case 2	9-Nov-2015 09:20	
Test Case 3	9-Nov-2015 09:20	
Test Case 4	9-Nov-2015 09:20	
Test Case 5	9-Nov-2015 09:20	
Total: 5		
Coverage data		
Test Case	Requirement	Model

<http://www.all4tec.net/MaTeLo/homematelo.html>

## DIVERSITY - METHODOLOGY



## COUPLING DIVERSITY AND MATELO



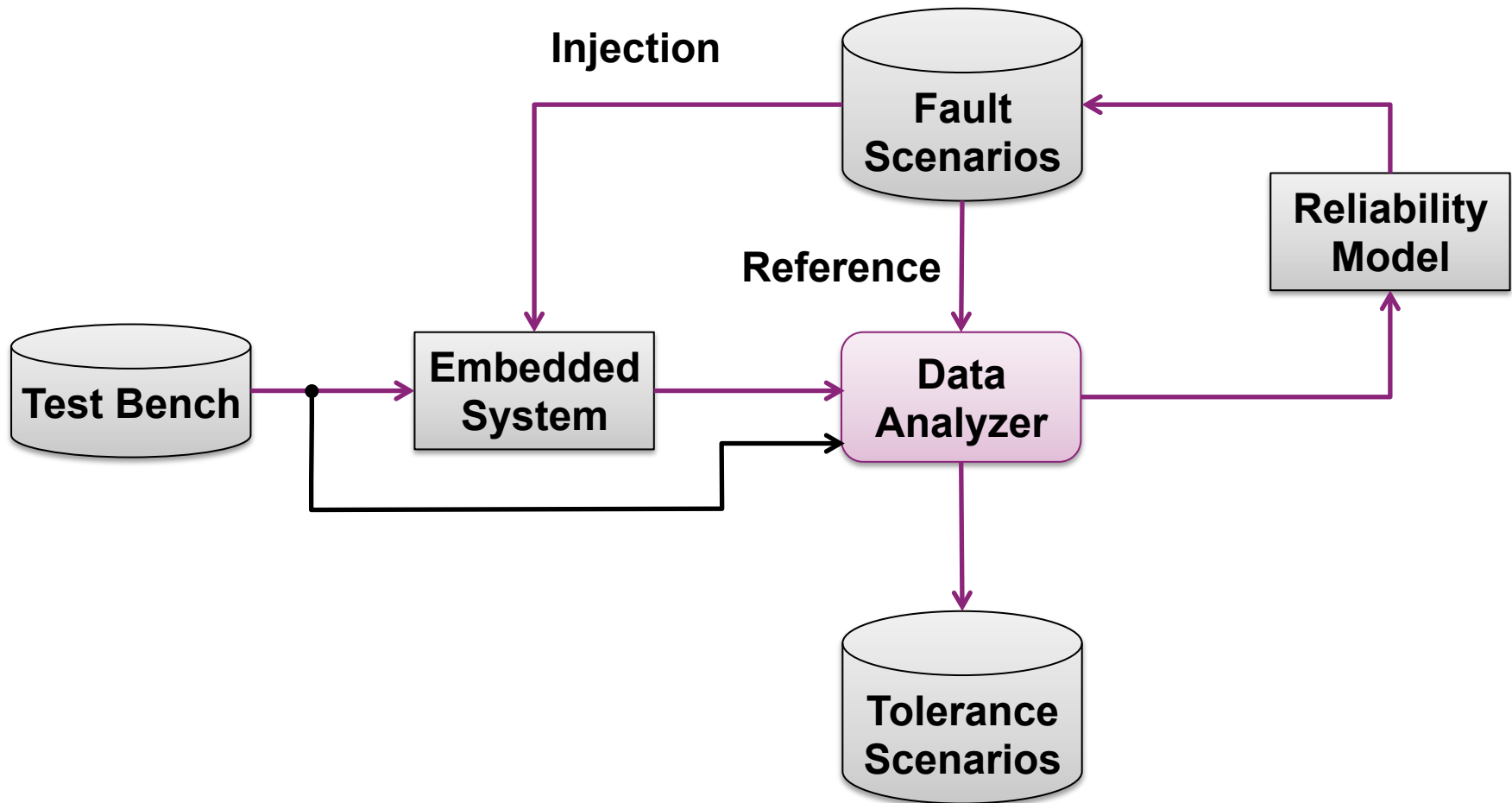
### ■ Remove duplicates (40%)

- ⇒ Preserving software reliability
- ⇒ Enhancing test coverage

### ■ Reduction of overall test length

- ⇒ Reducing duration of test execution
- ⇒ Reducing cost
- ⇒ Preserving same test coverage

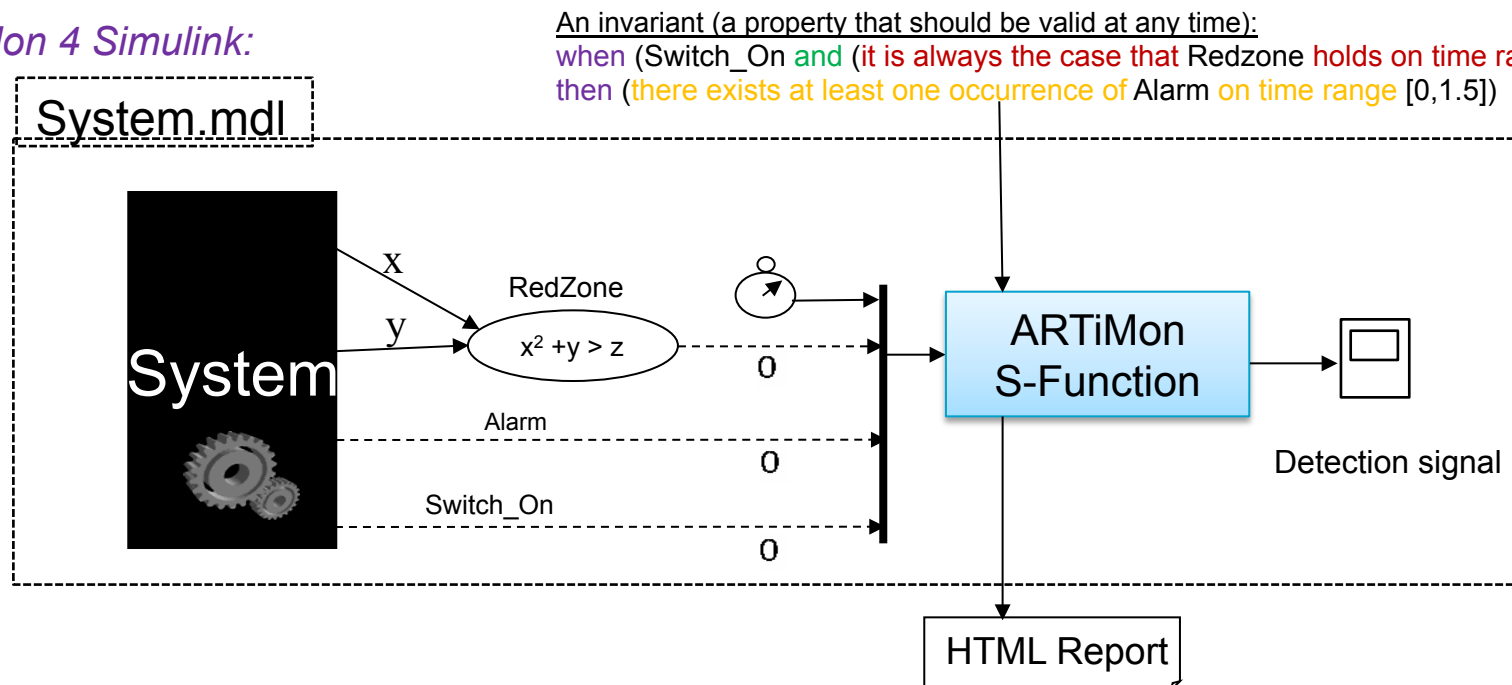
## FUNCTIONAL SAFETY AND RELIABILITY ANALYSIS



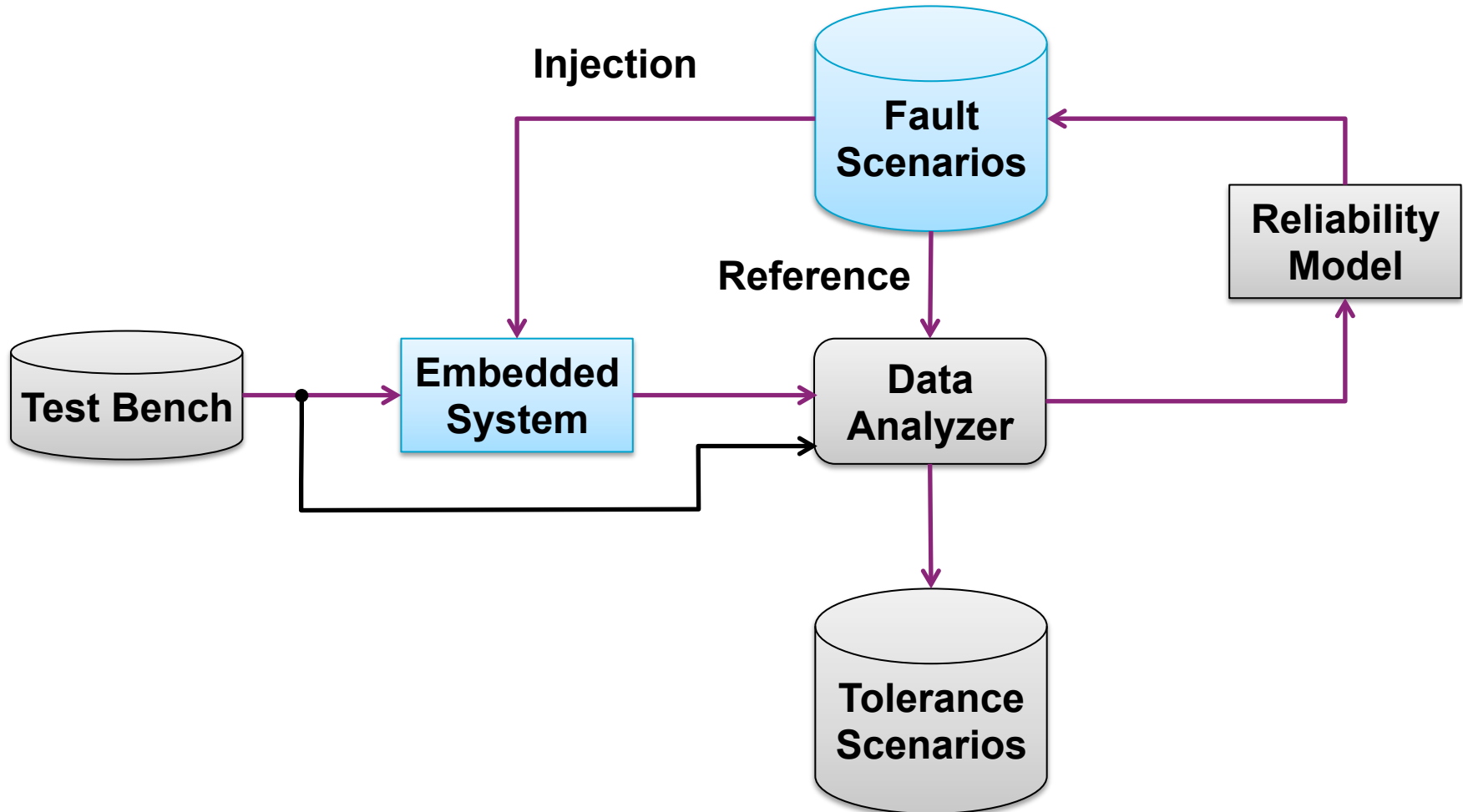
## ARTIMON: ADVANCED REAL TIME INFORMATION MONITORING

- Provides a temporized logic based language
  - To express requirements about system real-time behavior
- Transforms a set of requirements into operational detectors
  - For the simulation/execution environment

### ARTiMon 4 Simulink:

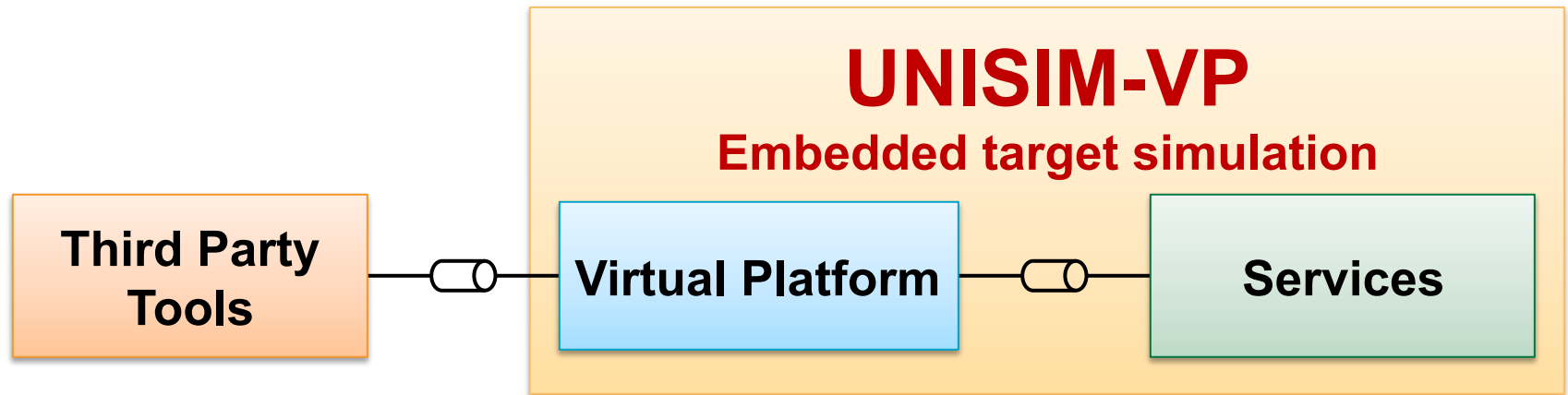


## FUNCTIONAL SAFETY AND RELIABILITY ANALYSIS





## COMPONENT-BASED VIRTUALIZATION ENVIRONMENT



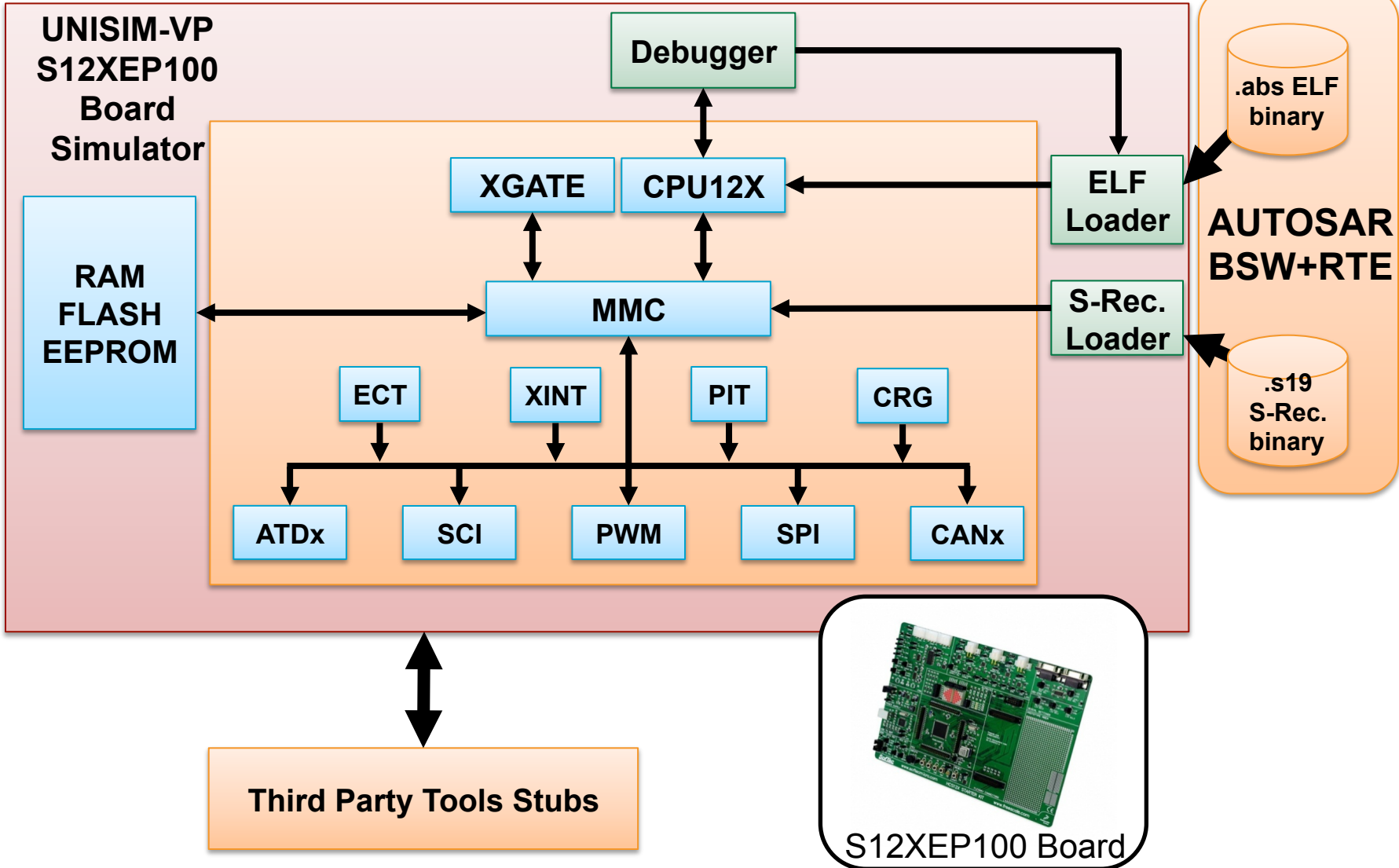
- Co-simulation
- Test-bench database
- Existing tool-chains

- System on Chip
- Boards
- SystemC/TLM

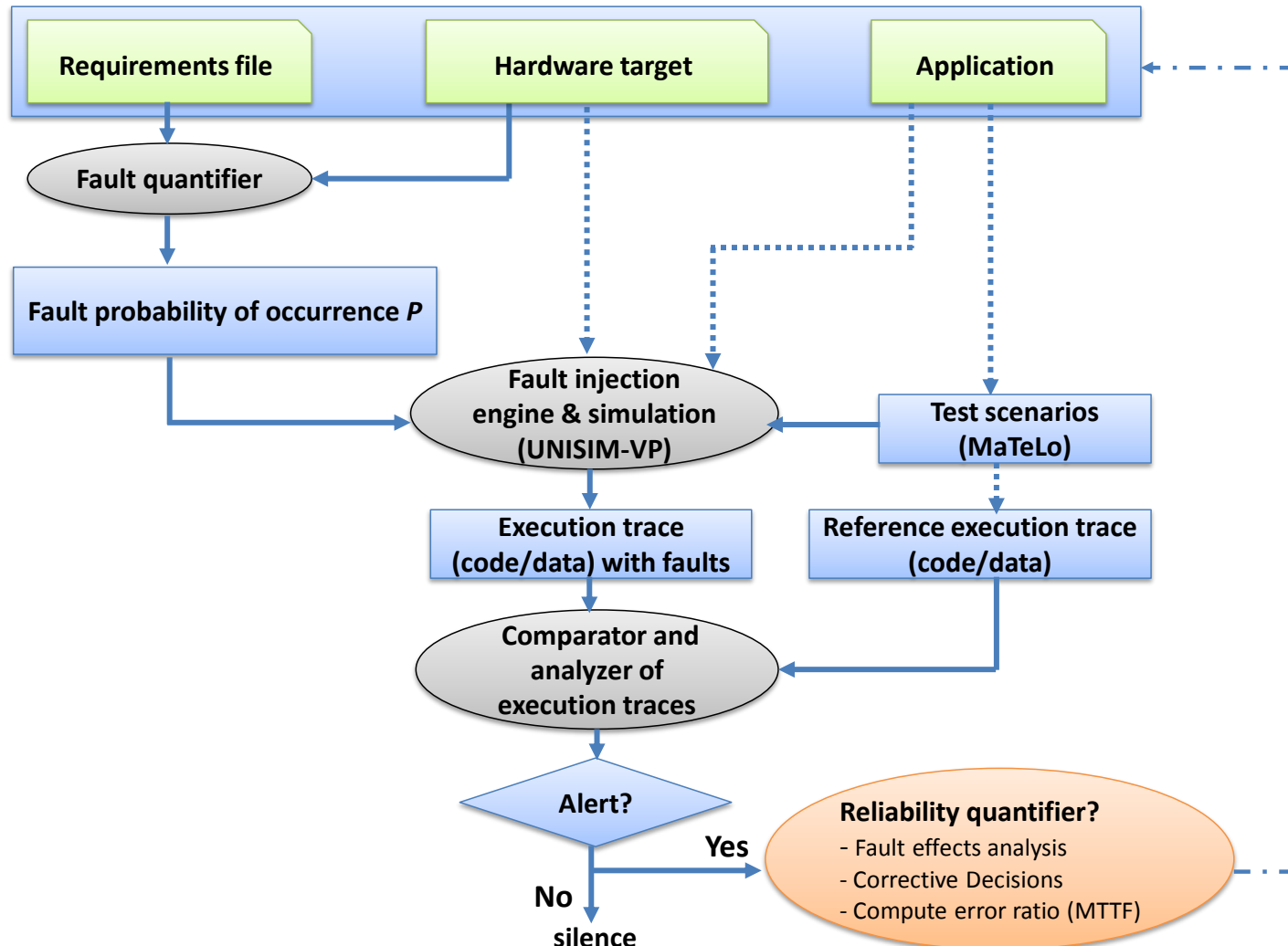
- Debugger
- Test
- Monitor
- Trace analysis
- Profiling
- HW fault injection

<https://unisim-vp.org/site/index.html>

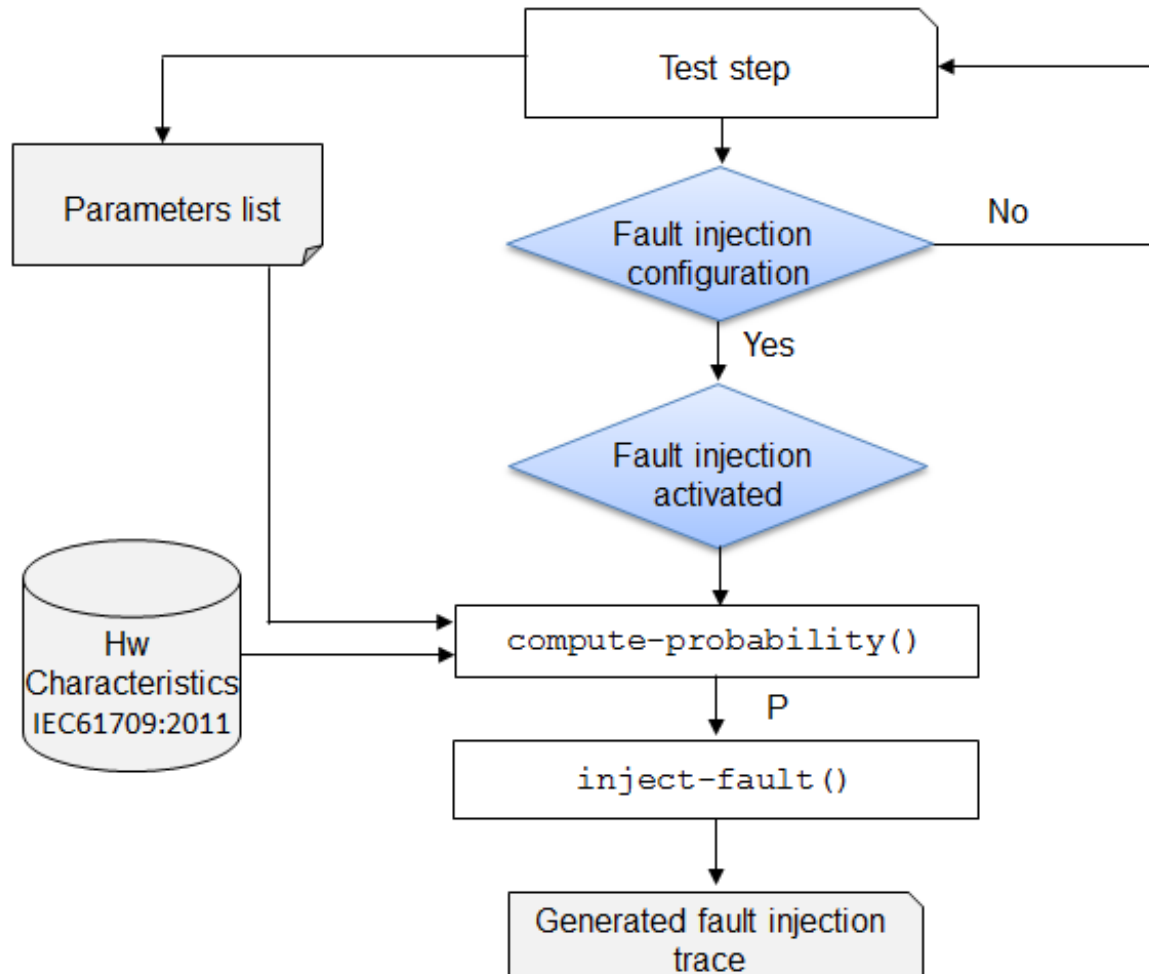
# STAR12X – AUTOMOTIVE APPLICATION

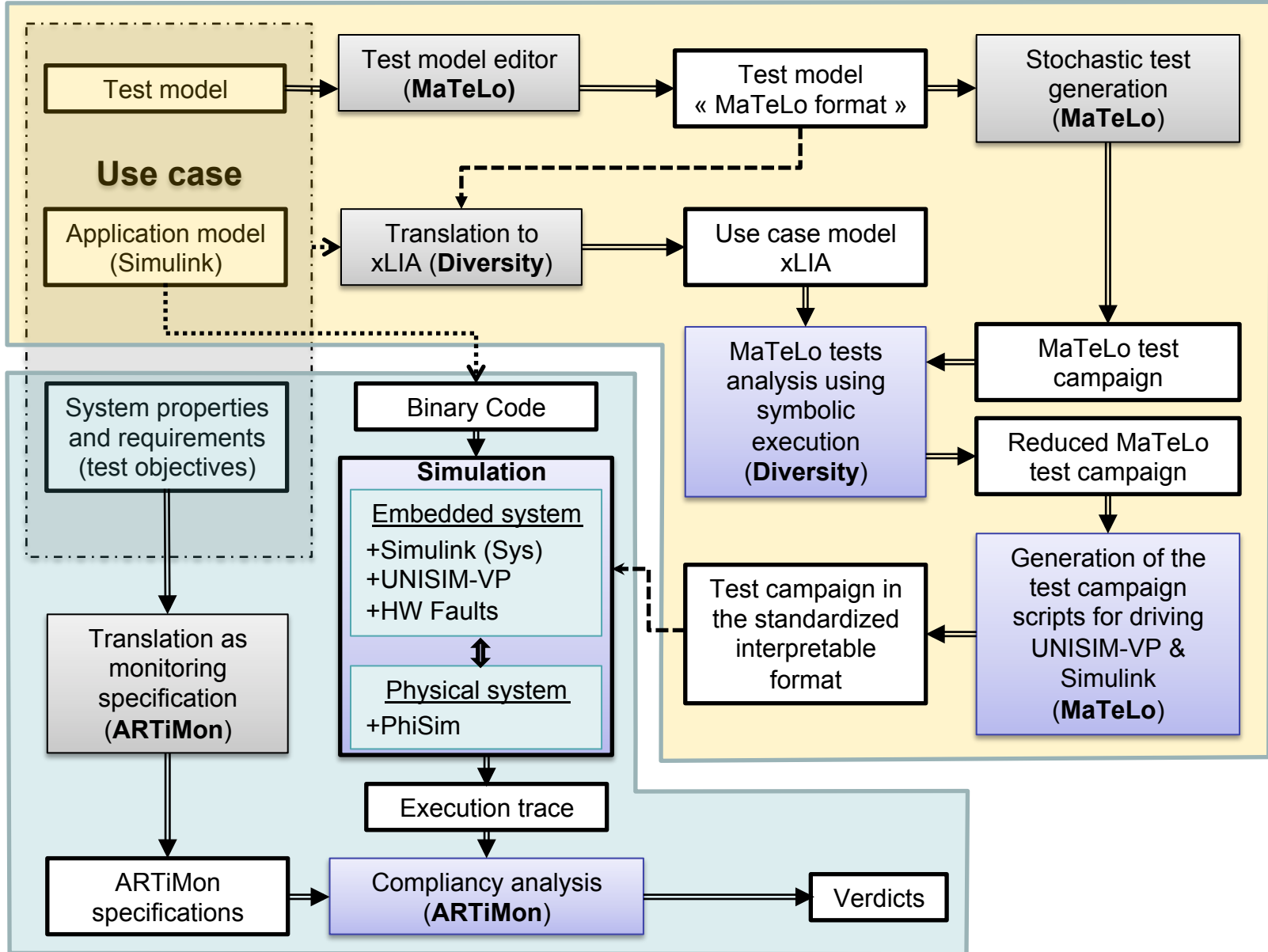


## TEST CASE ENHANCED WITH HARDWARE FAULT INJECTION

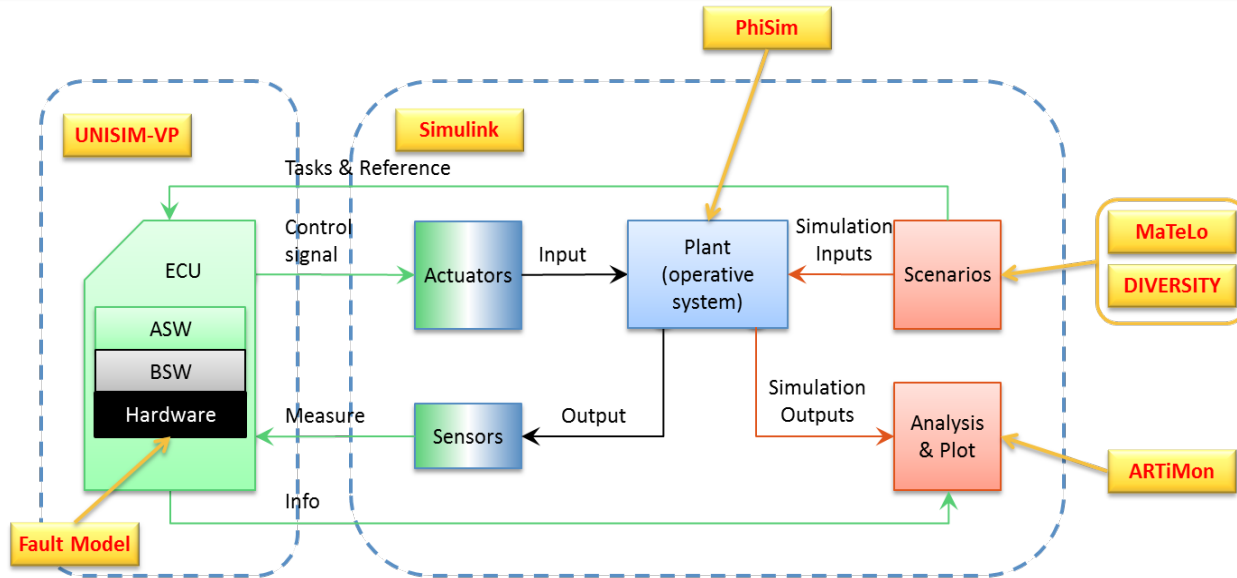
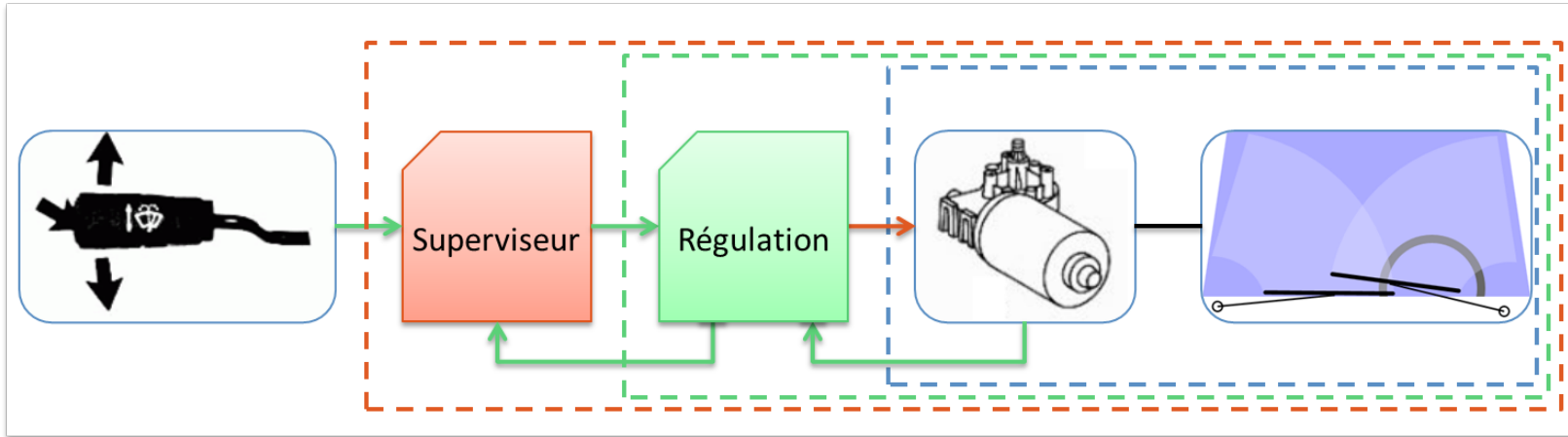


## FAULT INJECTION STRATEGY





# WINDSHIELD WIPER



## NEXT WORKS

- **Studying the compliancy of the EQUITAS toolchain with the ISO26262 standard.**
- **Quantitative and qualitative assessment of the toolchain, by the project industrial partners.**
- **Generalization of project activities**
  - Distributed embedded systems
  - Heterogeneous (model, ASW, binary) validation and verification
- **Extension of EQUITAS toolchain**
  - RAMS analysis (Reliability, Availability, Maintainability, Safety)



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